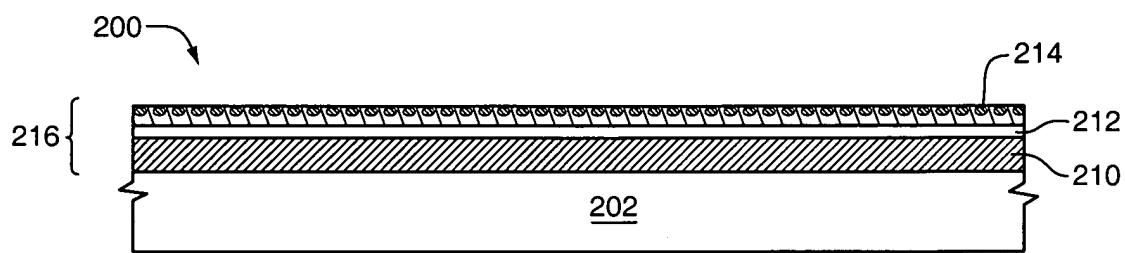
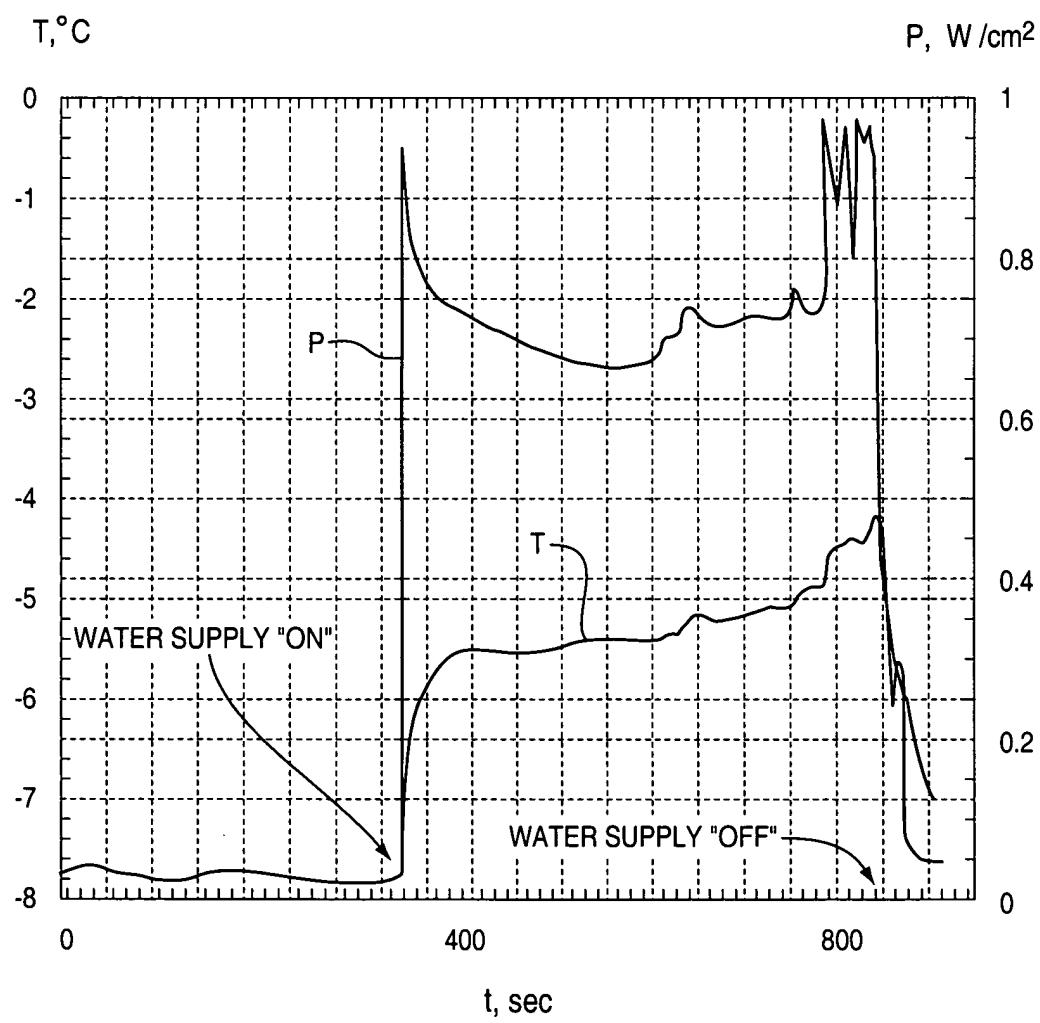


**FIG. 1**



**FIG. 2**

FIG. 2  
A cross-sectional diagram of a device structure. At the top, a layer labeled 200 is shown with an arrow pointing to it. Below it is a stack of layers: 216 (hatched), 212 (diagonal lines), and 210 (solid). A central region is labeled 202. A bracket on the left side of the stack is labeled 216, and a bracket on the right side is labeled 214.



**FIG. 3**



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REPLACEMENT DRAWING SHEET

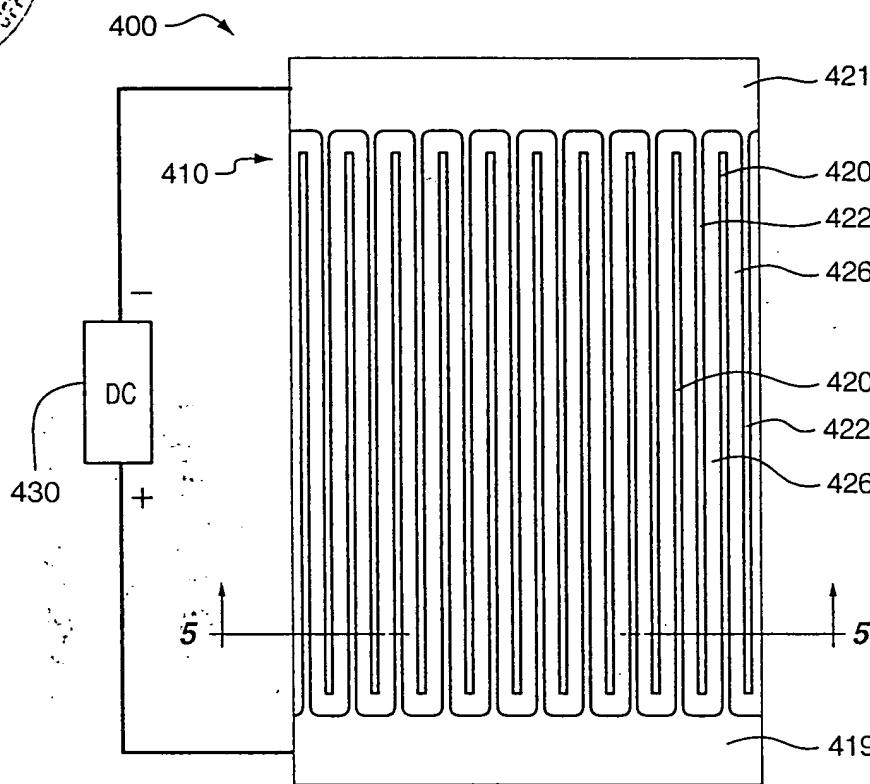


FIG. 4

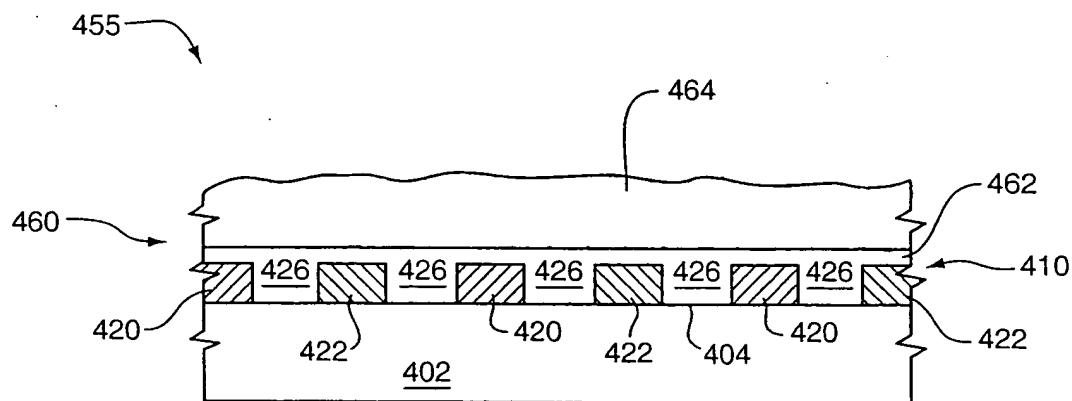
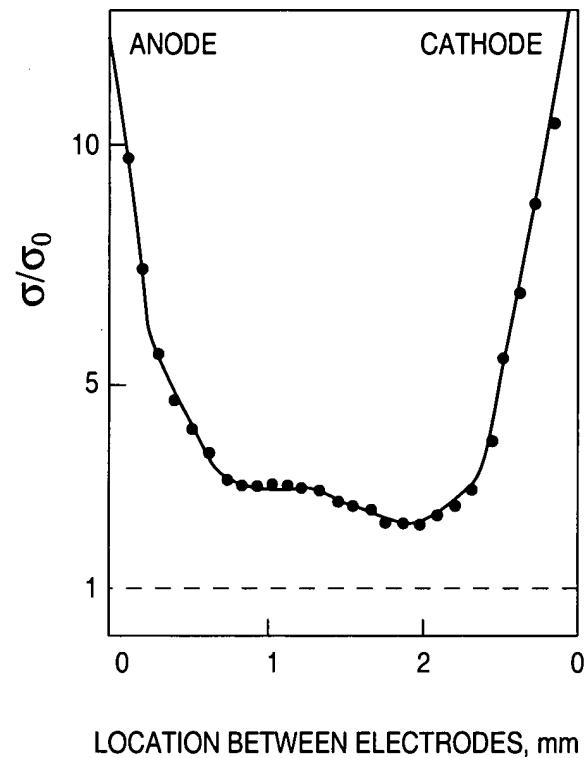


FIG. 5



**FIG. 6**